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NOV 1 4 2005

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Applicant:

Robert M. Steinhoff, et al.

Docket Number: TI-35703

Serial No.: 10/690,006

Art Unit: 2811

Filed: 10/21/03

Examiner: Andrew O. Arena

ESD Protection for Integrated Circuits

CERTIFICATION OF FACSIMILE TRANSMISSION

I hereby certify that the following papers are being transmitted by facsimile to the U.S. Patent and Trademark Office on the date shown below:

FACSIMILE COVER SHEET

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NAME OF INVENTOR(S):		RECEIPT DATE & SERIAL NO.:
Robert M. Steinhoff, et al.		Serial No.: 10/690,006
TITLE OF INVENTION: ESD Protection for Integrated Circuits		Filing Date: 10/21/03
TI FILE NO.:	DEPOSIT ACCT. NO.:	
TI-35703	20-0668	
FAXED: 11-14-05 DUE: 12-6-05 ATTY/SECY: RAK/kJv		

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Texas Instruments Incorporated PO Box 655474, M/S 3999 Dallas, TX 75265-5474

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Title: ESD Protection for Integrated Circuits

AMENDMENT UNDER 37 CFR §1.111

November 14, 2005

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450 CERTIFICATION OF FACSIMILE TRANSMISSION

I hereby certify that the above correspondence is being transmitted by facsimile to the U.S. Patent and Trademark Office at 571-273-8300 on the date shown below.

Karen Kert

11-14-05

Dear Commissioner:

In response to the Office Action, dated 09/06/2005, in the above-identified patent application, please make the following amendments. They are respectfully submitted as a full and complete response to that Action. Charge any required fees to the deposit account of Texas Instruments Incorporated, Account No. 20-0668.